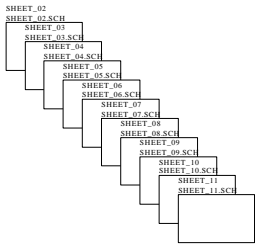


REFERENCE DESIGN

EVALUATION BOARD FOR STPC CLIENT

REV 1.1

APRIL 16, 1998



| FILENAME | SHEET DESCRIPTION |
|--------------|------------------------------------|
| SHEET_01.SCH | COVER |
| SHEET_02.SCH | STPC CLIENT |
| SHEET_03.SCH | DRAM |
| SHEET_04.SCH | VGA, VIP |
| SHEET_05.SCH | NTSC/PAL ENCODER |
| SHEET_06.SCH | ROM BIOS, CONFIGURATION STRAPS |
| SHEET_07.SCH | 3 PCI SLOTS |
| SHEET_08.SCH | 3 ISA SLOTS + IRQ, DRQ,DACK MUX |
| SHEET_09.SCH | SIO, SERIALS, PARALLEL, KBD, MOUSE |
| SHEET_10.SCH | FLOPPY / EIDE |
| SHEET_11.SCH | POWER SUPPLY, SPEAKER |

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PRELIMINARY AND SUBJECT TO CHANGE WITHOUT NOTICE.
SGS-THOMSON BEARS NO RESPONSIBILITY FOR ANY ERRORS IN
THESE SCHEMATIC SHEETS.

AUTHORS: THIERRY SEIGNEURIE

REVISION HISTORY:

REV 0.1: GLORIA 1.1
REV 1.0: IDE cleaned up.
REV 1.1: ISA Fix for 8-bit Memory transfers in MASTER mode.

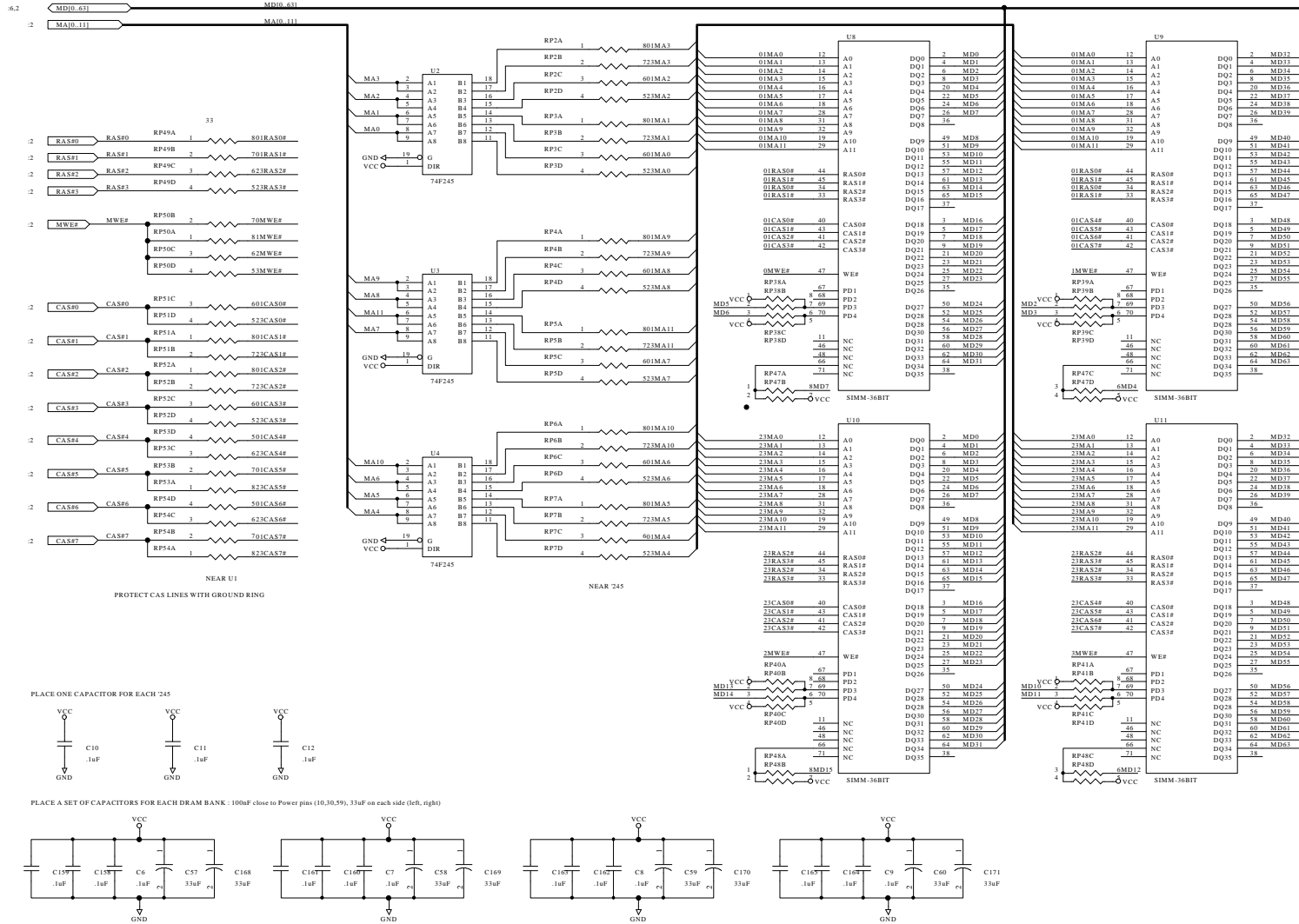
SGS-THOMSON MICROELECTRONICS
165 RUE EDOUARD BRANLY
91630 SAINT-GENIS-POUILLY FRANCE

SA00002AAXx

PROTEL CONFIGURATION : PORT GLOBAL ONLY

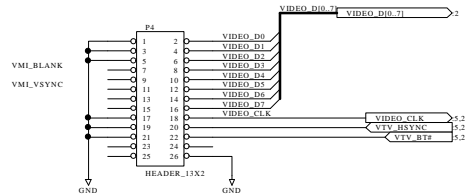
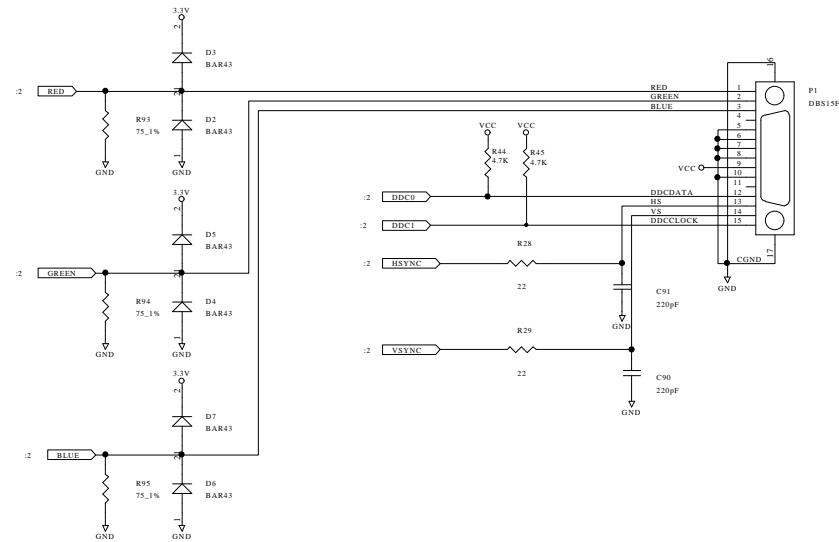
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DRAM 0,1,2,3



| | | |
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| Date: | 26 Mar 1998 | Sheet of |
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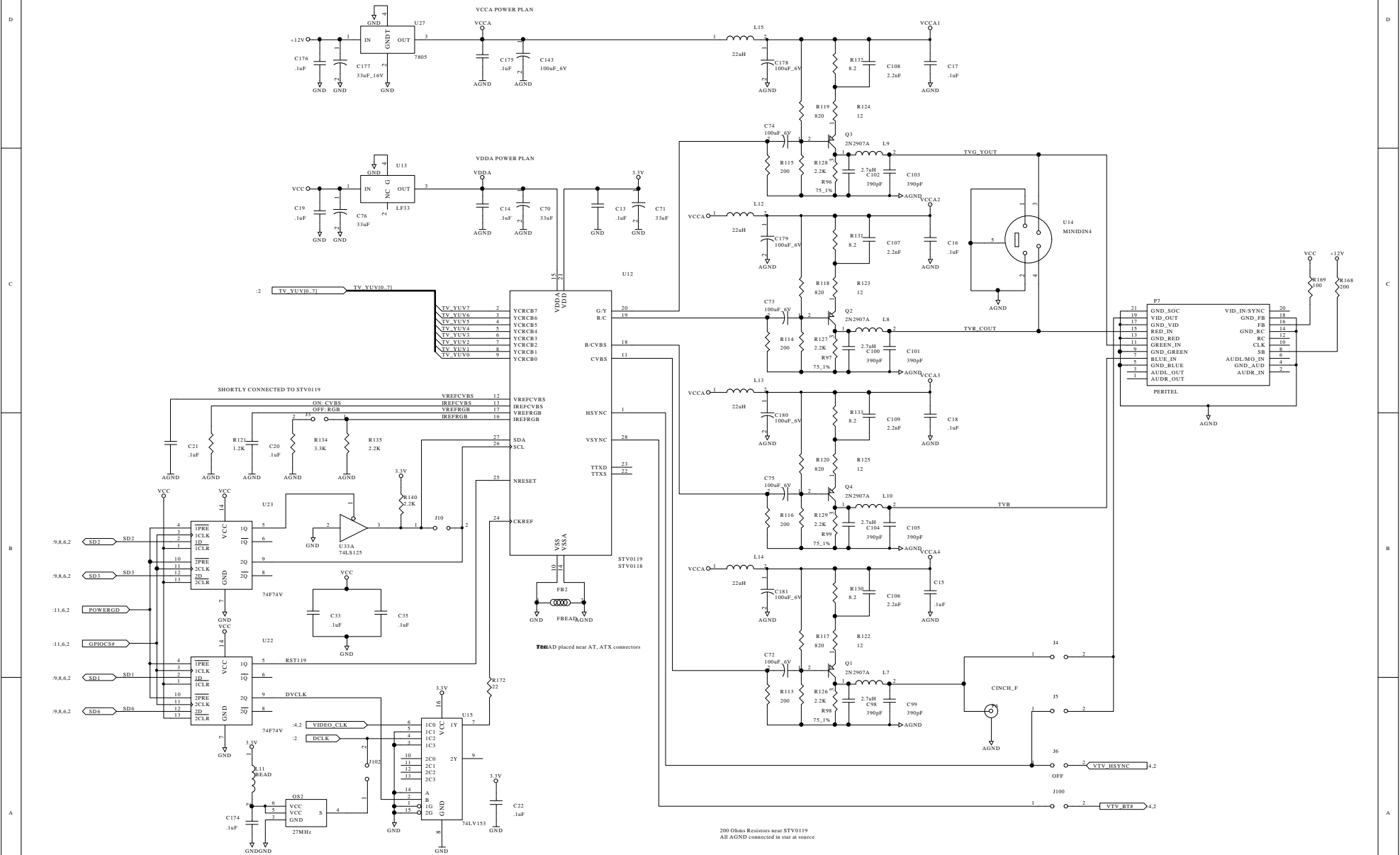
VGA, VIDEO INPUT PORT



DCLK AND VIDEO_CLK PROTECTED WITH GROUND RING

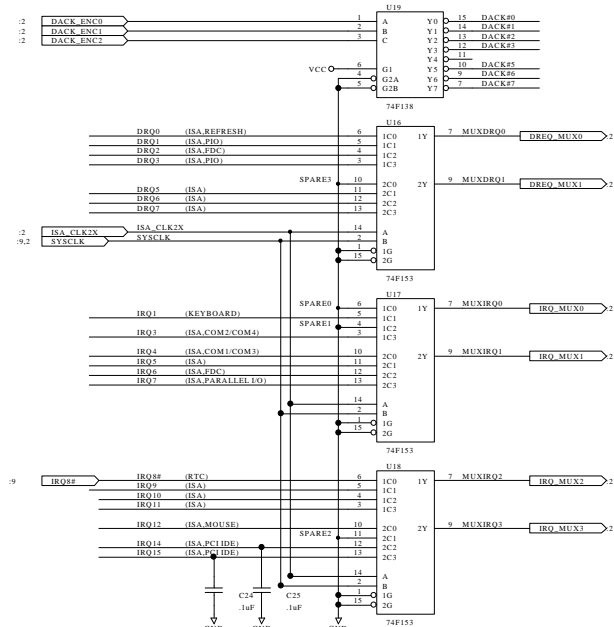
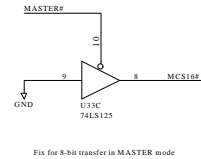
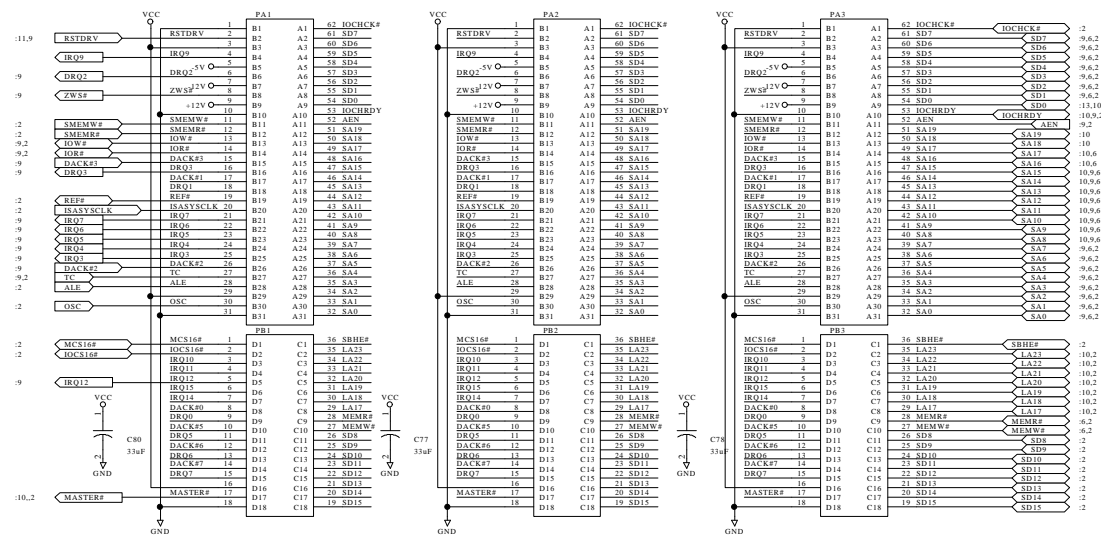
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NTSC/PAL ENCODER

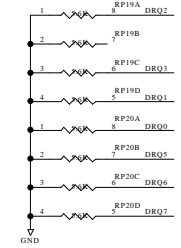
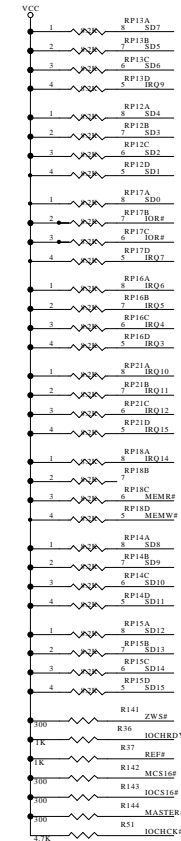
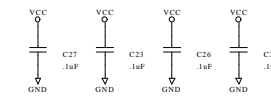


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3 ISA SLOTS, IRQ, DRQ, DACK MUX

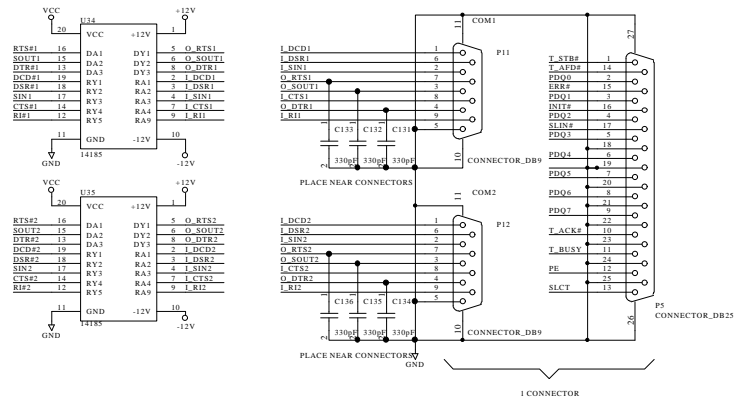
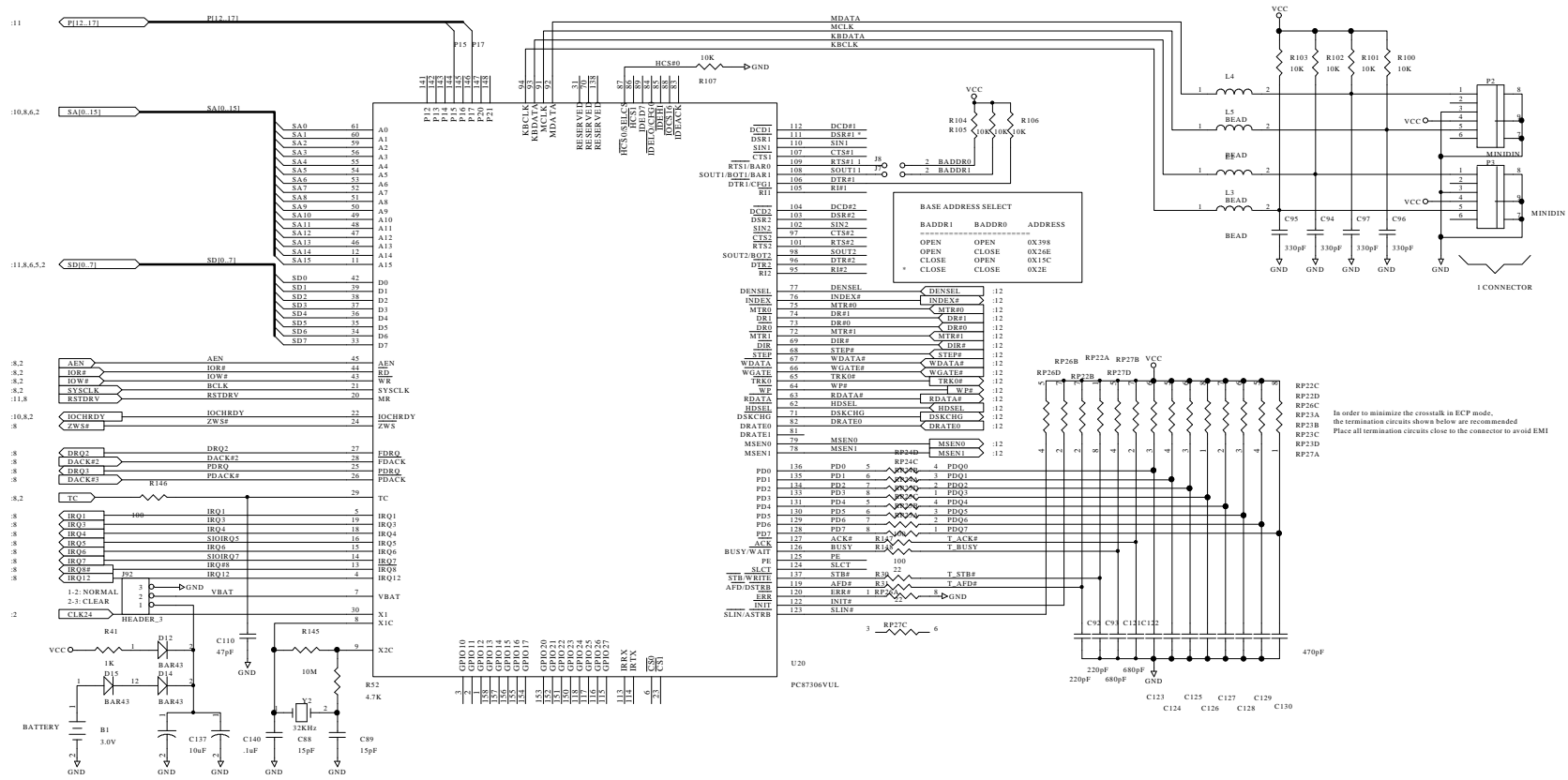


ONE CAPACITOR FOR EACH COMPONENT



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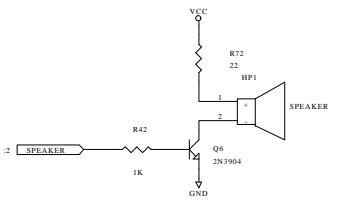
SUPER I/O



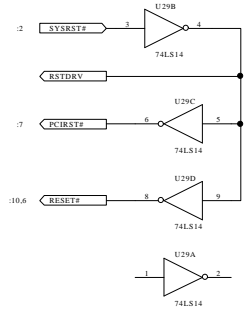
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POWER SUPPLY, SPEAKER

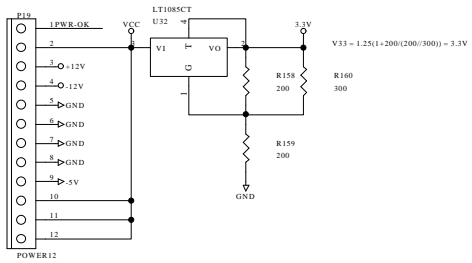
SPEAKER



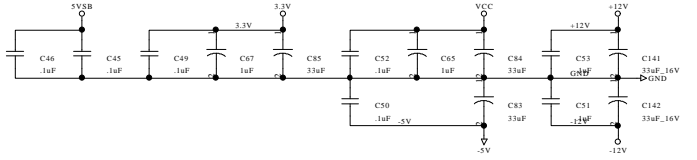
RESET SIGNALS



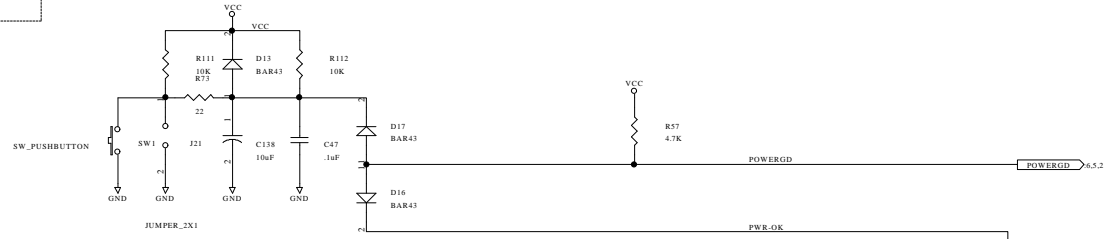
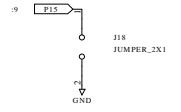
USE A LOCAL POWER PLAN TO CONNECT VI TO VCC



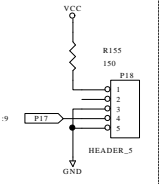
POWER DECOUPLING



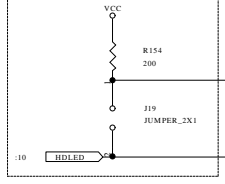
TURBO



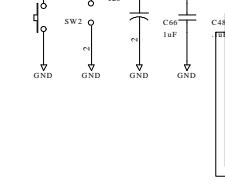
KEYLOCK



HDLED



SW_PUSHBUTTON



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|---------|----------------------|-----------|
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